Job ID: **PF42810A**
Job Title: Field-Programmable Gate Array (FPGA) Engineer
Degree Requirements: BSEE or MSEE
Years of Experience: 5 years with BSEE or 3 years with MSEE
Location: Washington State
Pay Rate: Open
Travel Required: Less than 20%

IMMEDIATE LONG TERM (1 year) CONTRACT NEED for FPGA Engineer who has experience in FPGA design and development.

The MUST HAVE ITEMS for this position are:
- MSEE with 3+ years or BSEE with at least 5 years of equivalent experiences in FPGA design and development
- At least 3 years experiences in Altera FPGA design and Digital Signal Processing (DSP) / embedded programming
- Experience with designing and implementing high-speed signal processing sub-systems on FPGAs
- Experience with Electronic Design Automation (EDA) tools and Integrated Drive Electronics (IDEs) used for FPGA and embedded processor development
- Proficiency with Verilog and VHDL
- Experience in implementation of fixed-point and floating-point arithmetics
- Excellent English written and verbal communication skills
- Ability to work in a team environment

The following with be pluses:
- Experience with Altera FPGA device family, Altera DSP Builder, and Altera Quartus II and its embedded IDE
- Experience with TI DSP device family such as C55x and TI C6x and Code Composer Studio Experience with high-speed wireless data communications products
- Familiarity with Matlab and Simulink environment
- Familiarity with Subversion version control software

Working with moderate supervision, you will be support the development of research prototypes; design and build up signal processing testbed for research works. This will include comprehending and analysis of FPGA architecture and usage of FPGA modules. Design, develop, test, and documenting code of signal processing sub-systems on FPGA, hybrid FPGA / DSP, or other hardware/software platforms in a real-time multithread operating system environment. You will debug and resolve FPGA component interfaces to relevant hardware and integrate the sub-system designs into baseline code.

The Major Duties are:
- Support Research and Development (R&D) work in signal processing and wireless communications
- Support development of research prototypes
- Design and develop HW and SW/Firmware to meet product and sub-system requirements
- Work with other department or outsource partners to develop and integrate sub-system deliverables
- Algorithm design, development, test, verify, and documenting of signal processing sub-systems on FPGA, hybrid FPGA/DSP, and other hardware/software platforms
- Design and build up signal processing research testbed, integrate design modules, and devise testing methods
- Utilize FPGA software and embedded processor design tools

Per Diem and Travel expenses may be available for this position.

If you meet these requirements and wish to be considered for this position, please send your résumé to us using Word 97 -2003 at Resumes@PinnaclePlacementGroup.com or call us at 770-422-2152 or 770-740-0494. Please mention the **Job ID** and the **Job Title** when contacting us.